

FIG. 1

100

108a, 108b, ..., 108m

108a

108b

...

108m

110

MUX

104

112

130

SWITCH ARRAY

134

136

133

DMUX

131

DMUX

132a, 132b, ..., 132m

132a

132b

...

132m

114

140

126

DWDM monitor

124

116

118

local loop

106

102

100



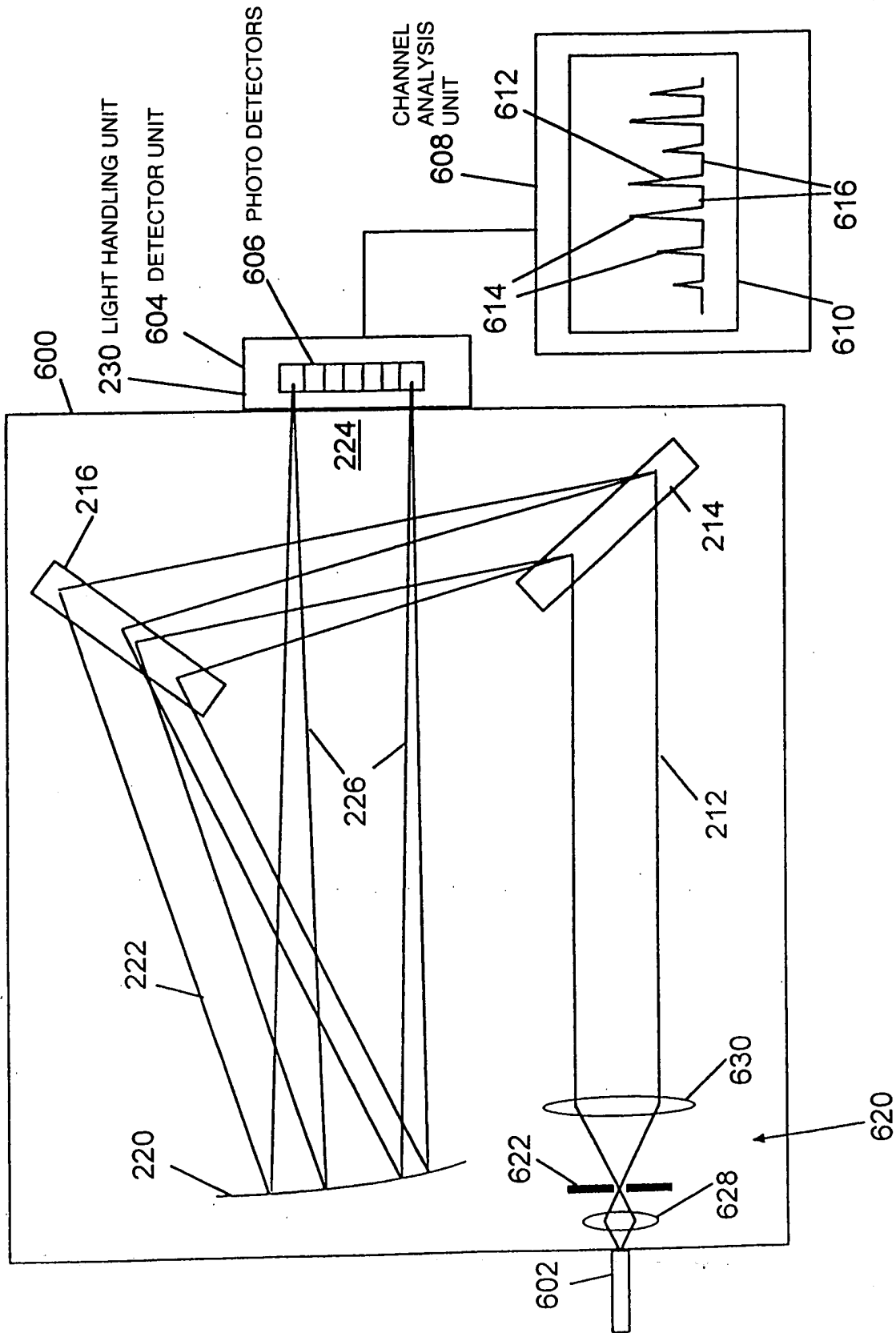


FIG. 6

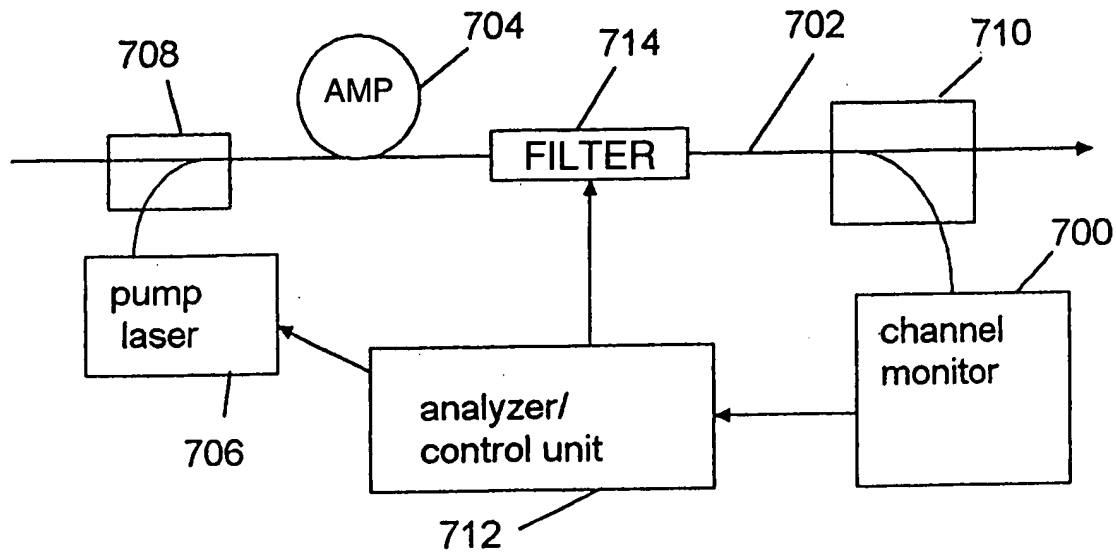


FIG. 7

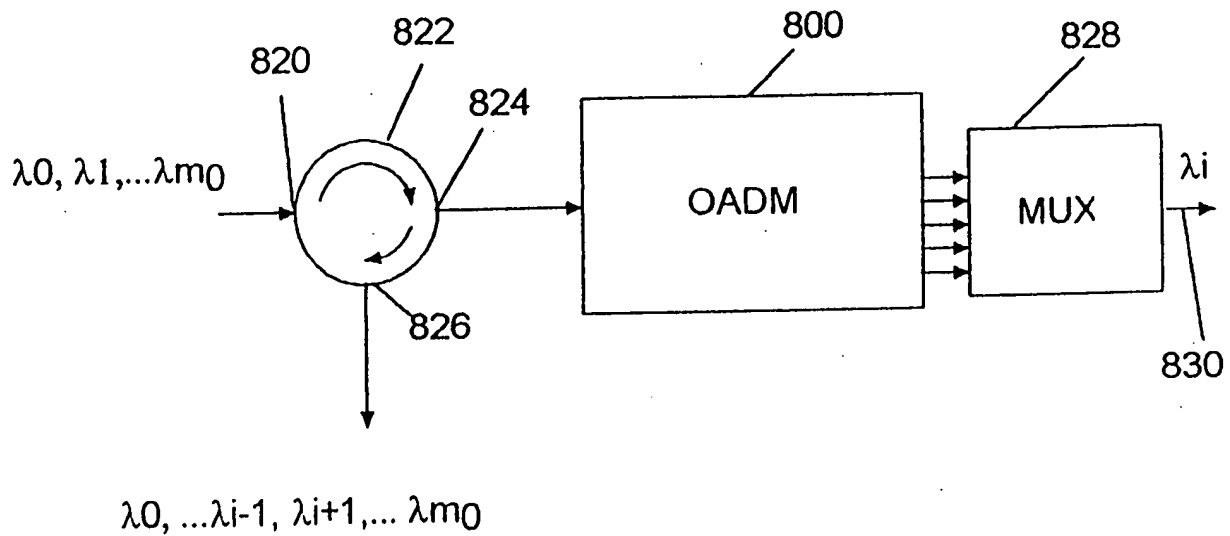


FIG. 10

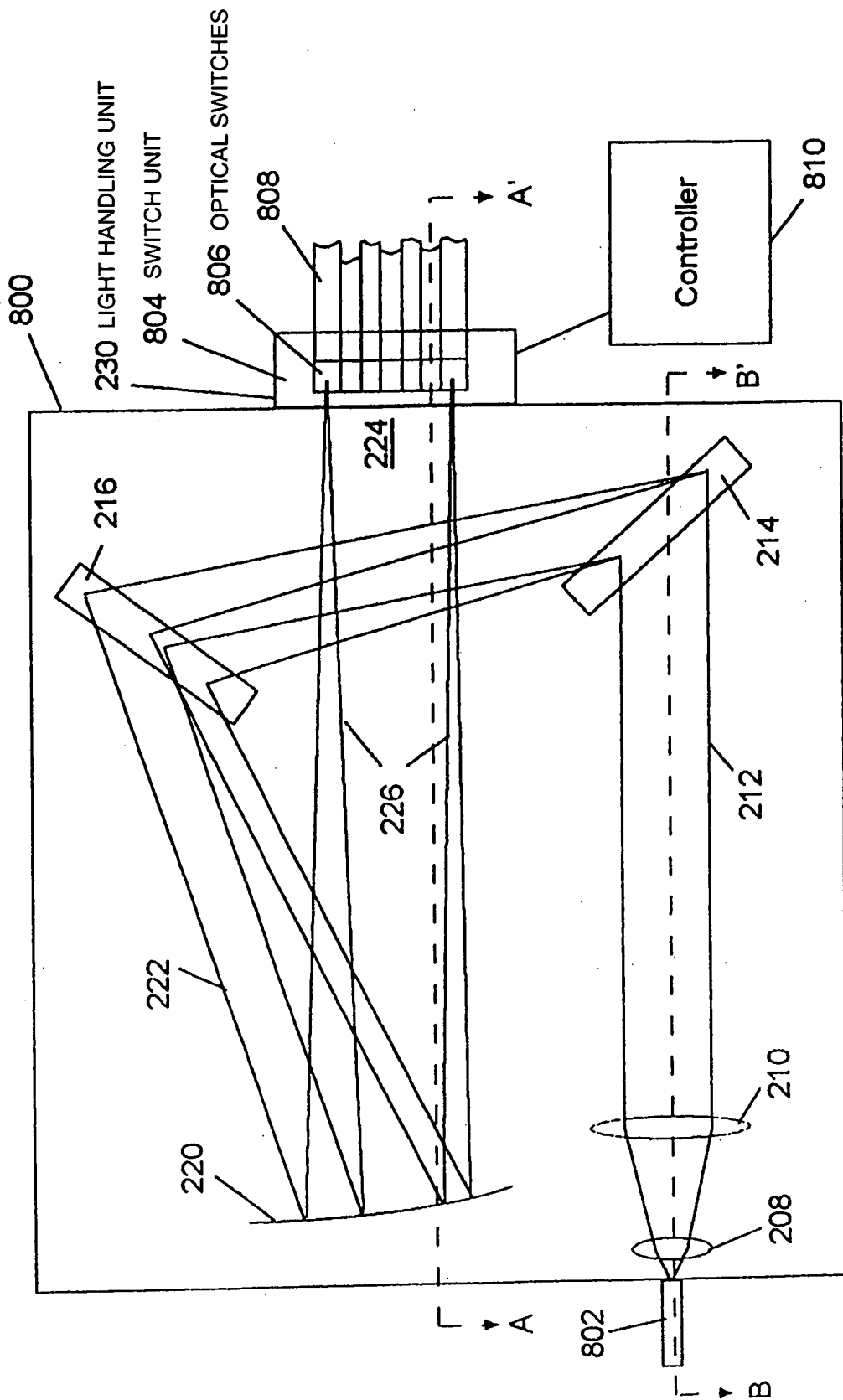


FIG. 8

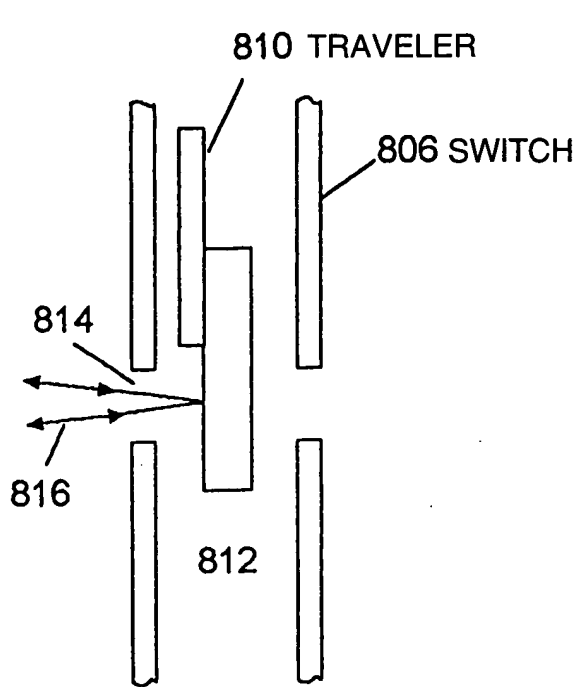


FIG. 9A

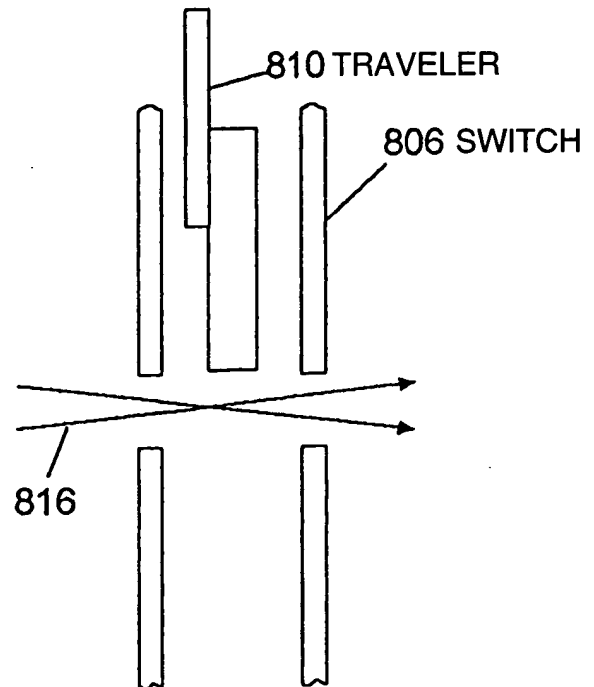


FIG. 9B

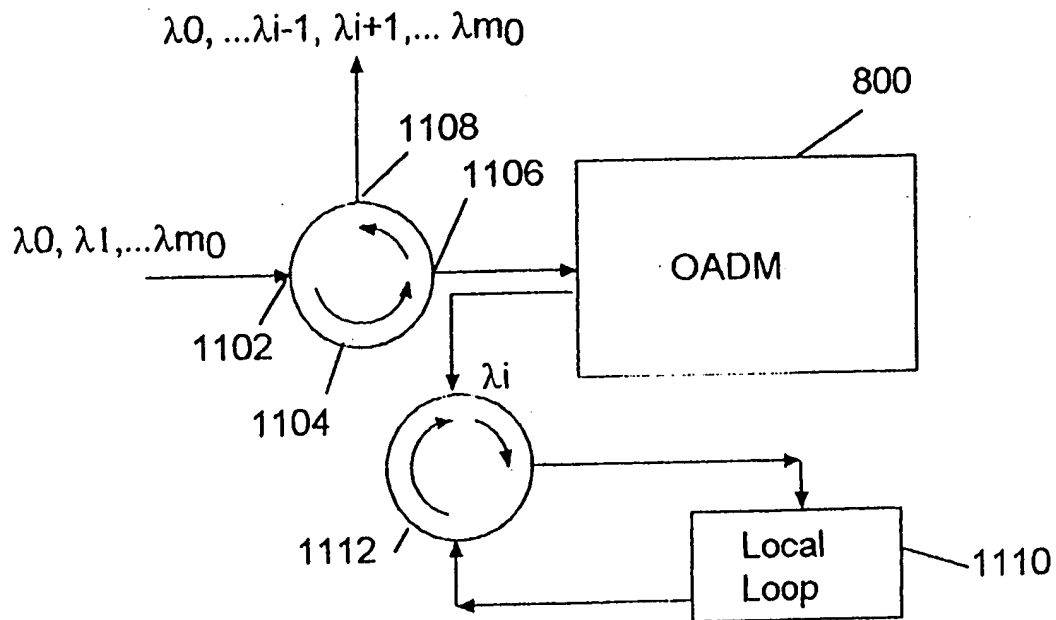
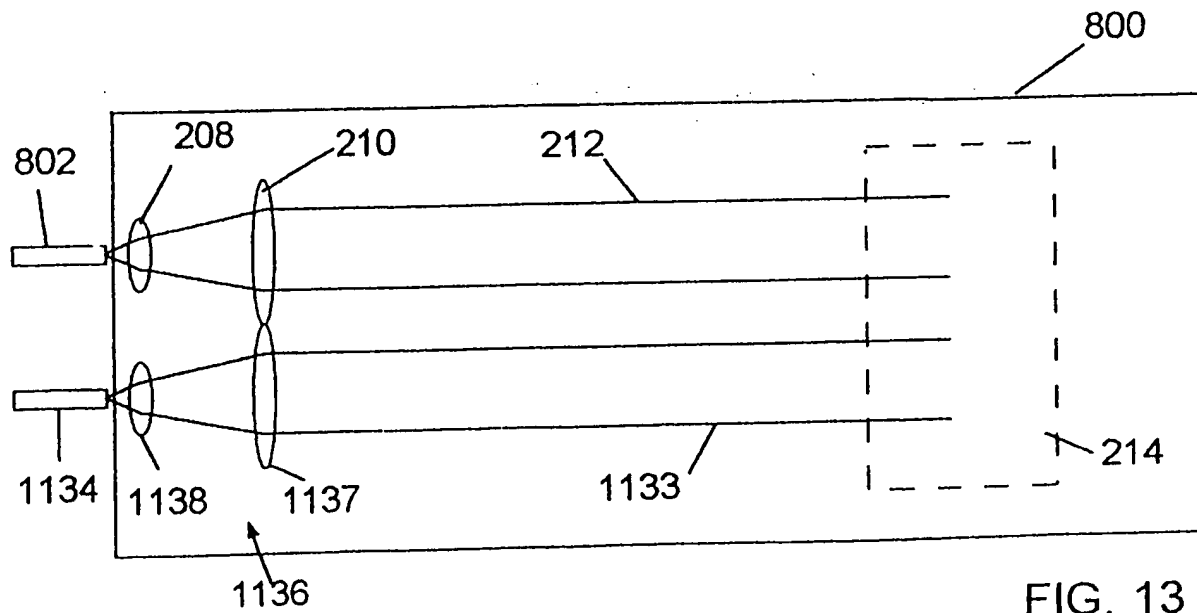
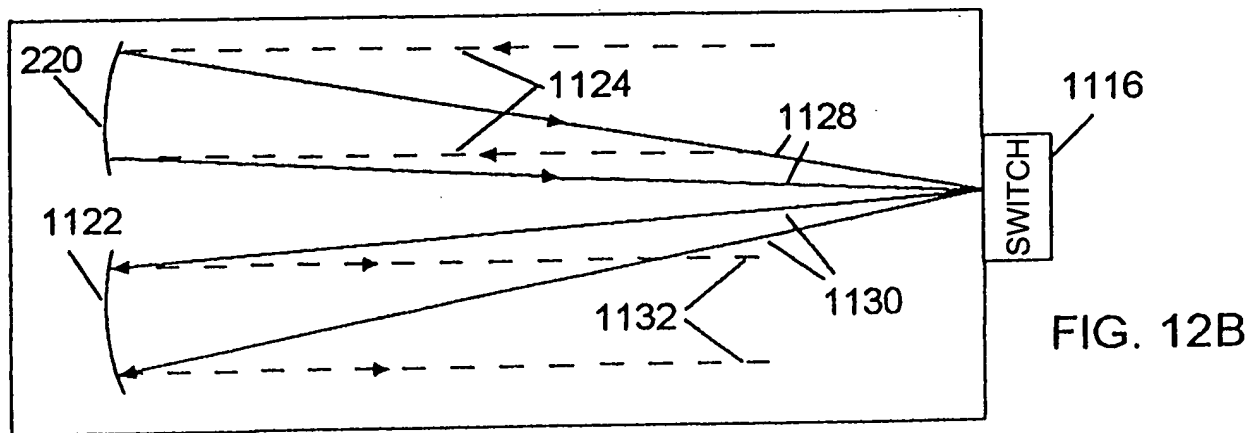
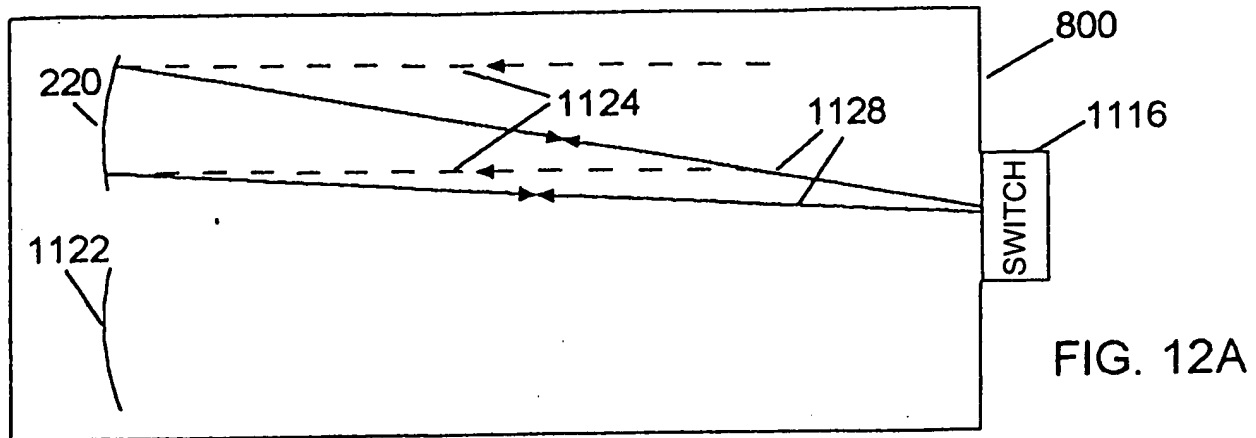


FIG. 11



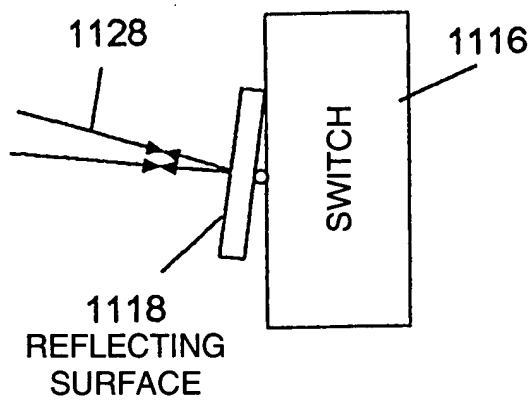


FIG. 14A

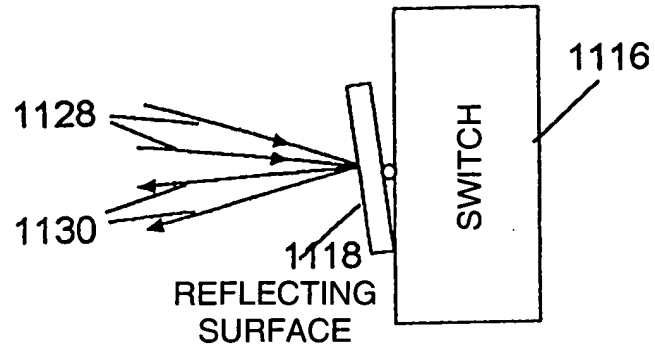


FIG. 14B

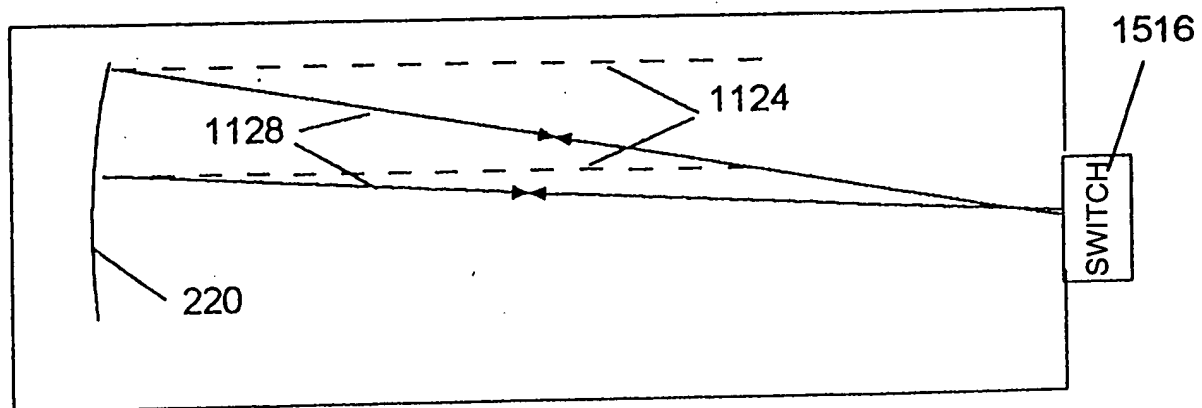


FIG. 15A

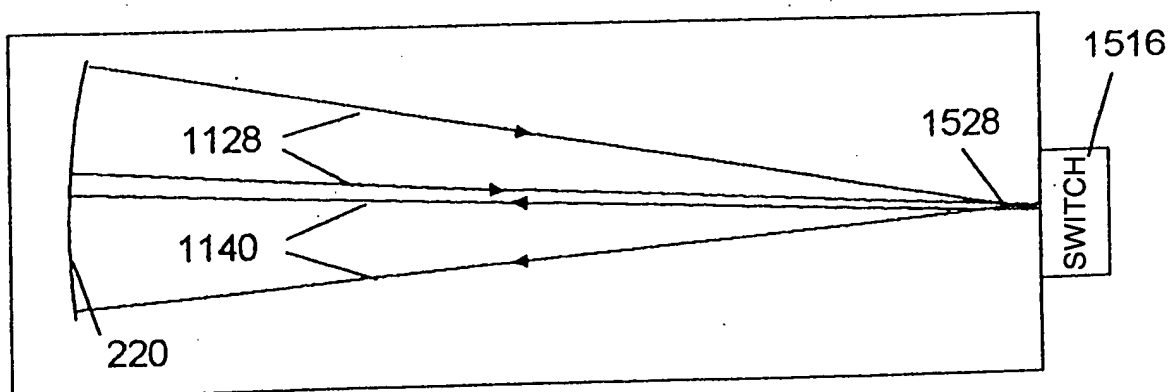


FIG. 15B



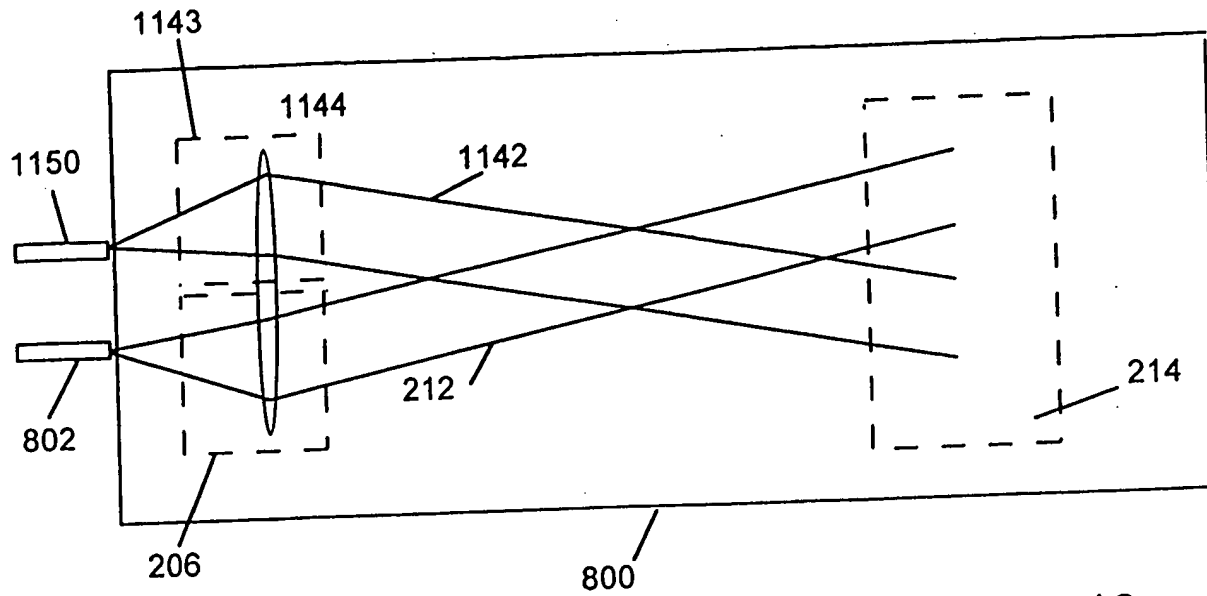


FIG. 16

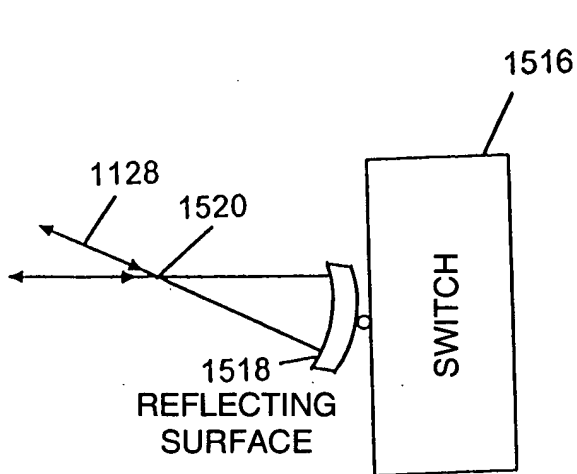


FIG. 17A

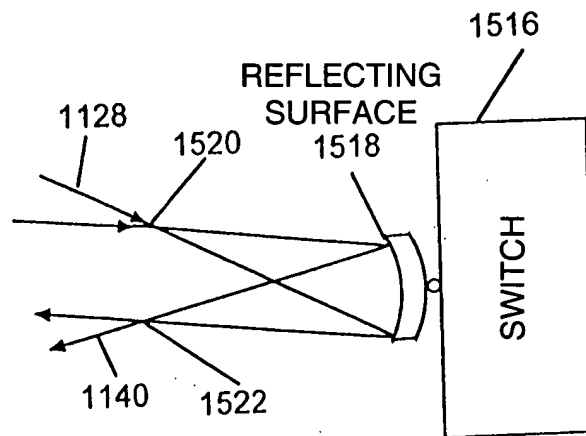
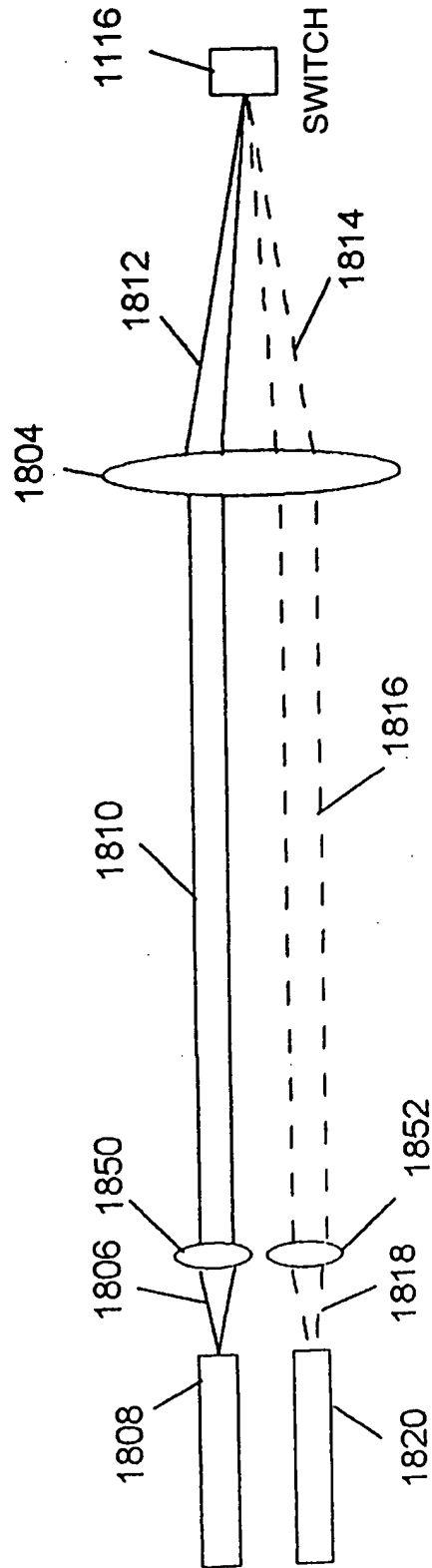
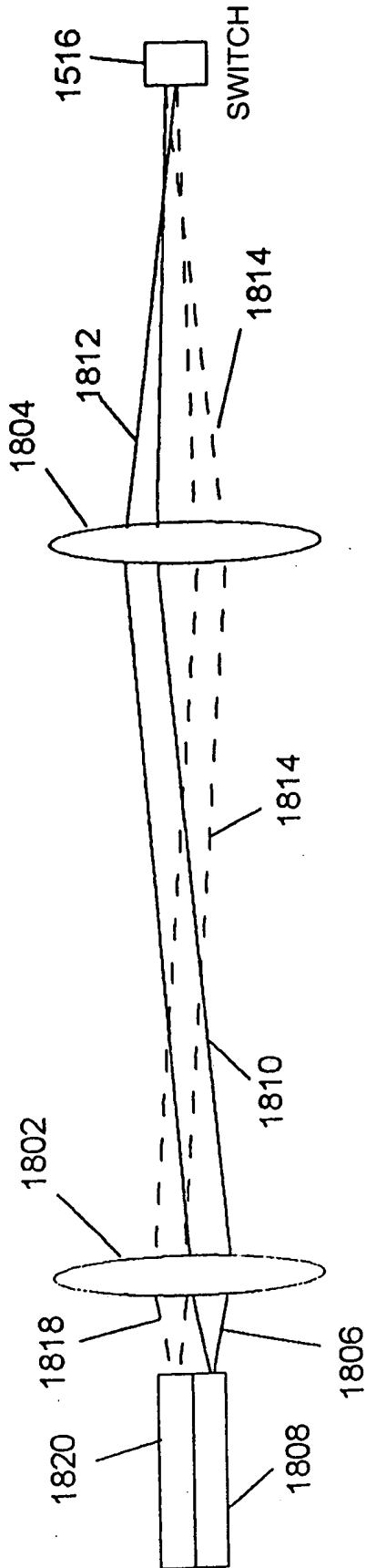


FIG. 17B



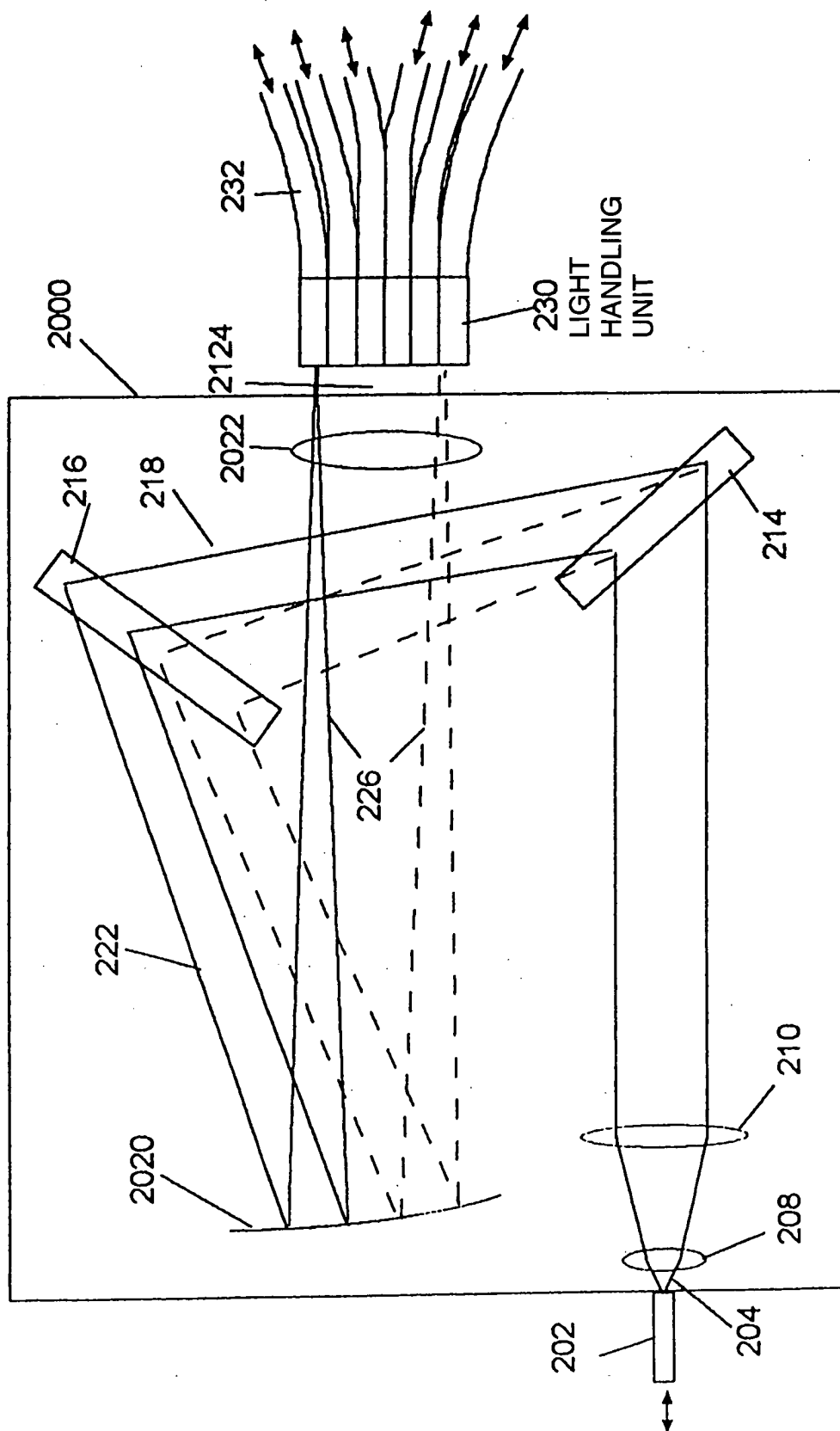


FIG. 20

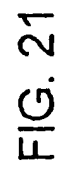


FIG. 21

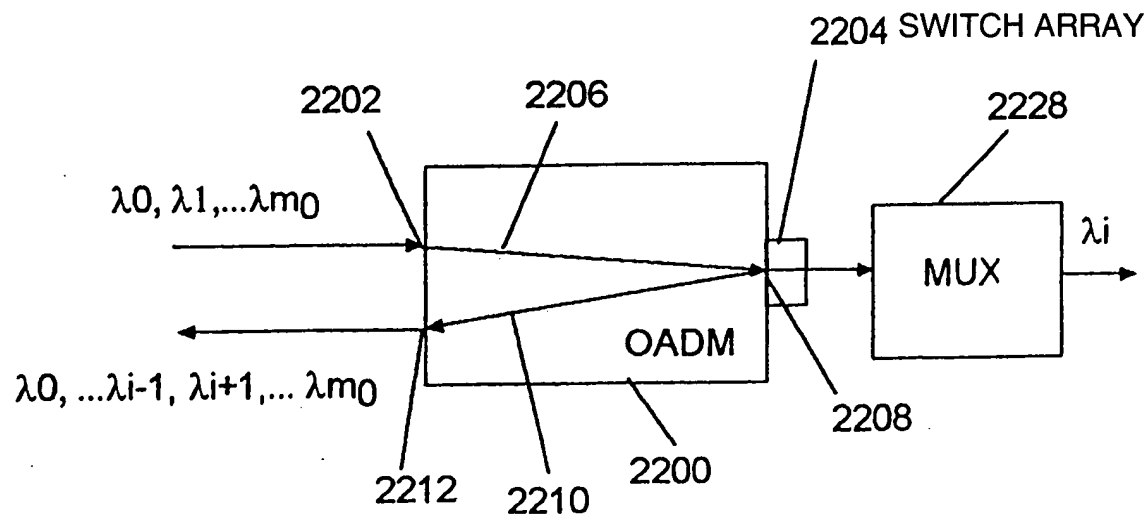


FIG. 22

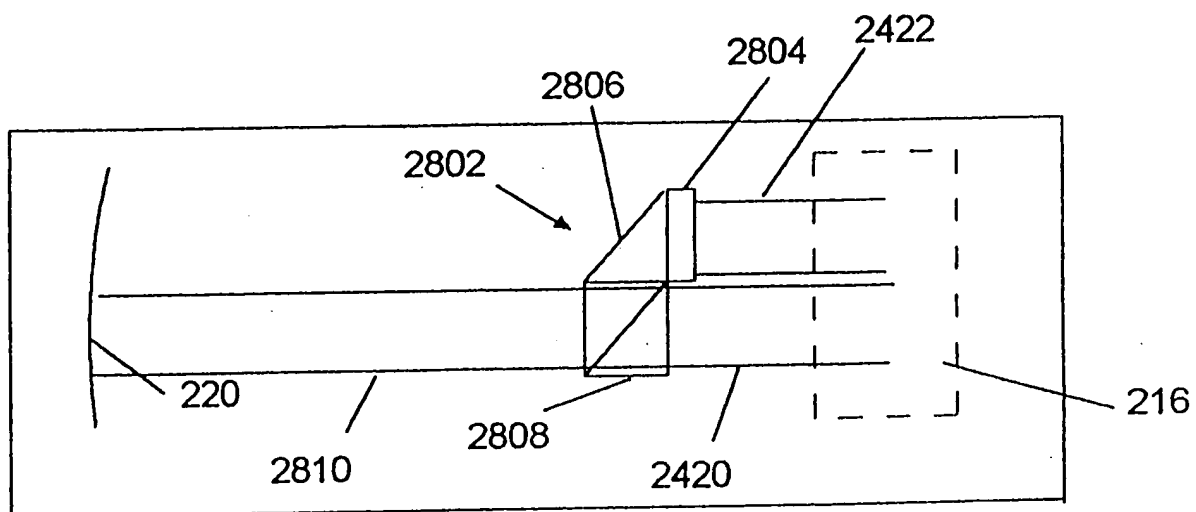


FIG. 29

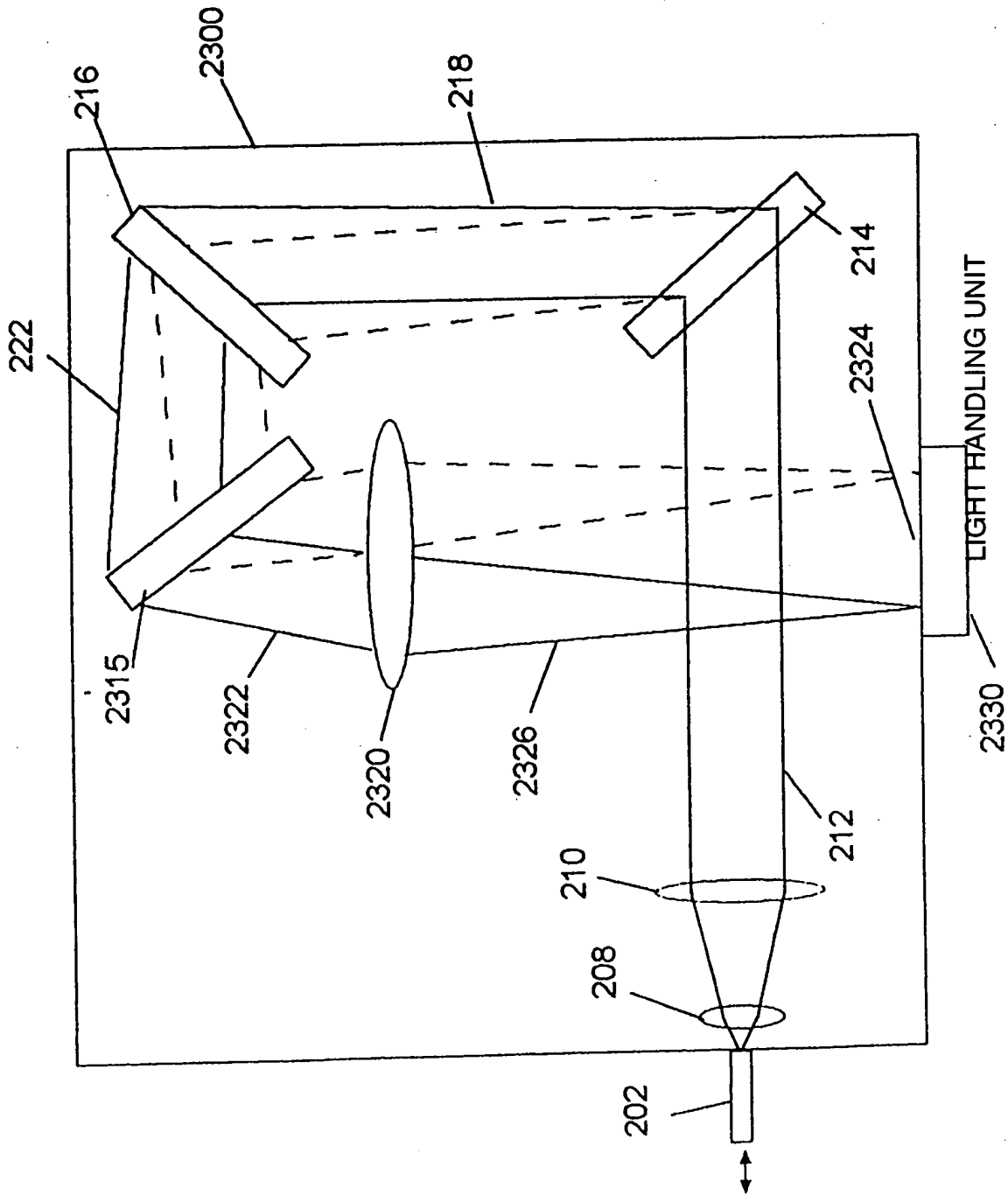


FIG. 23

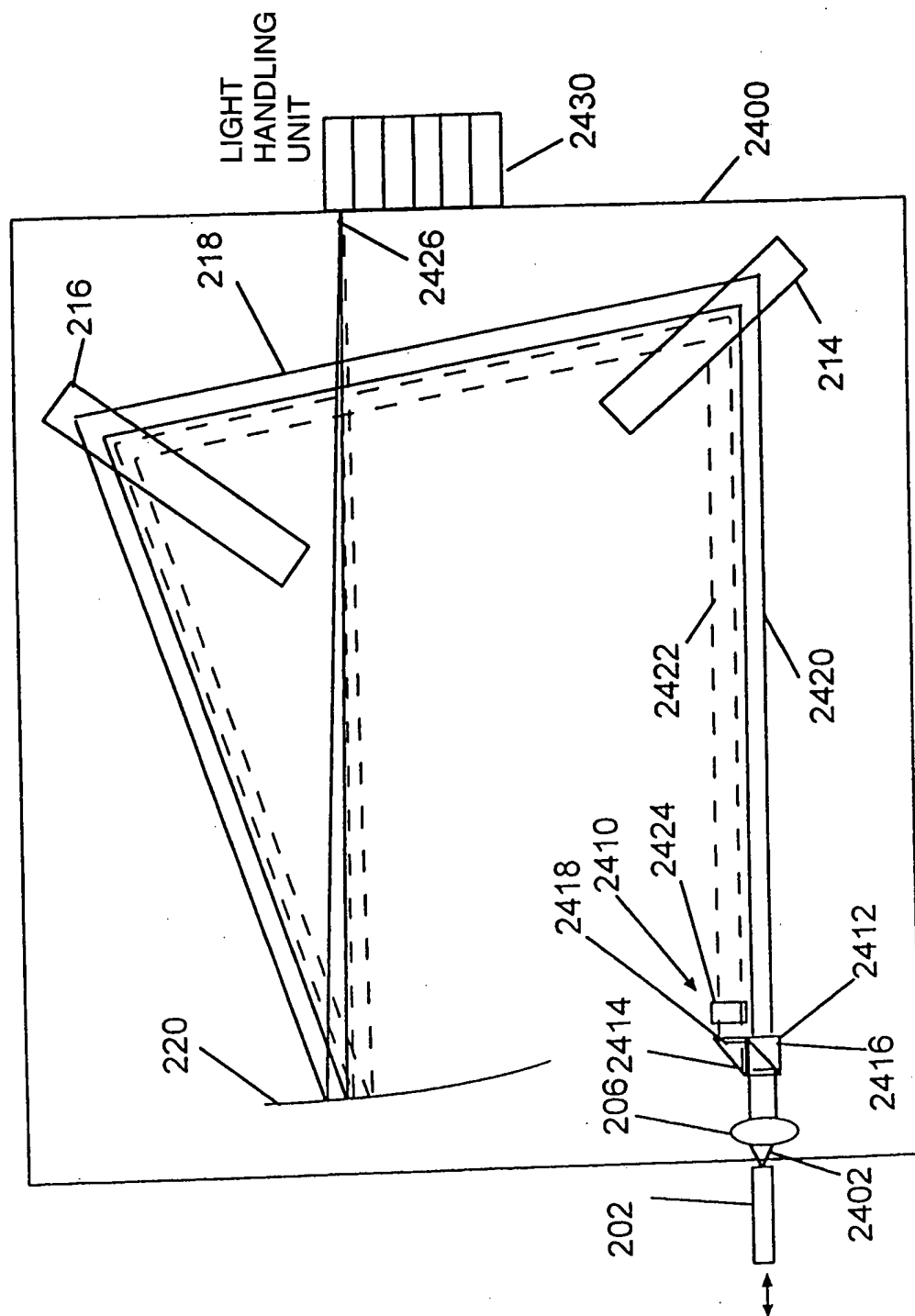


FIG. 24



FIG. 28



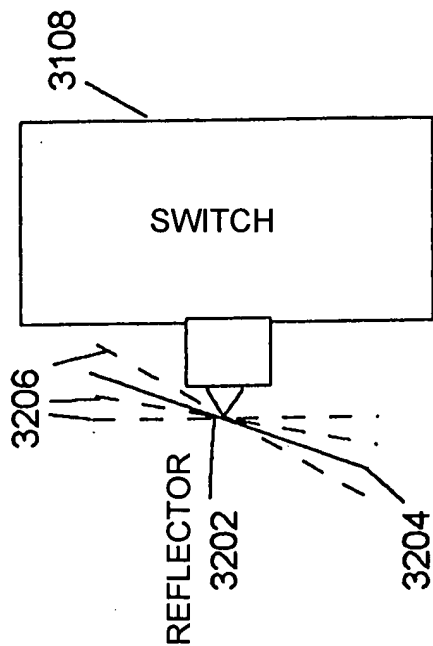


FIG. 32

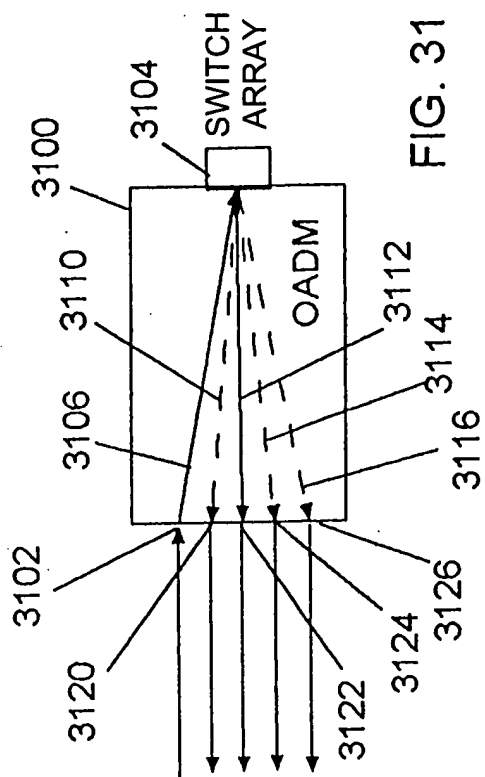


FIG. 31

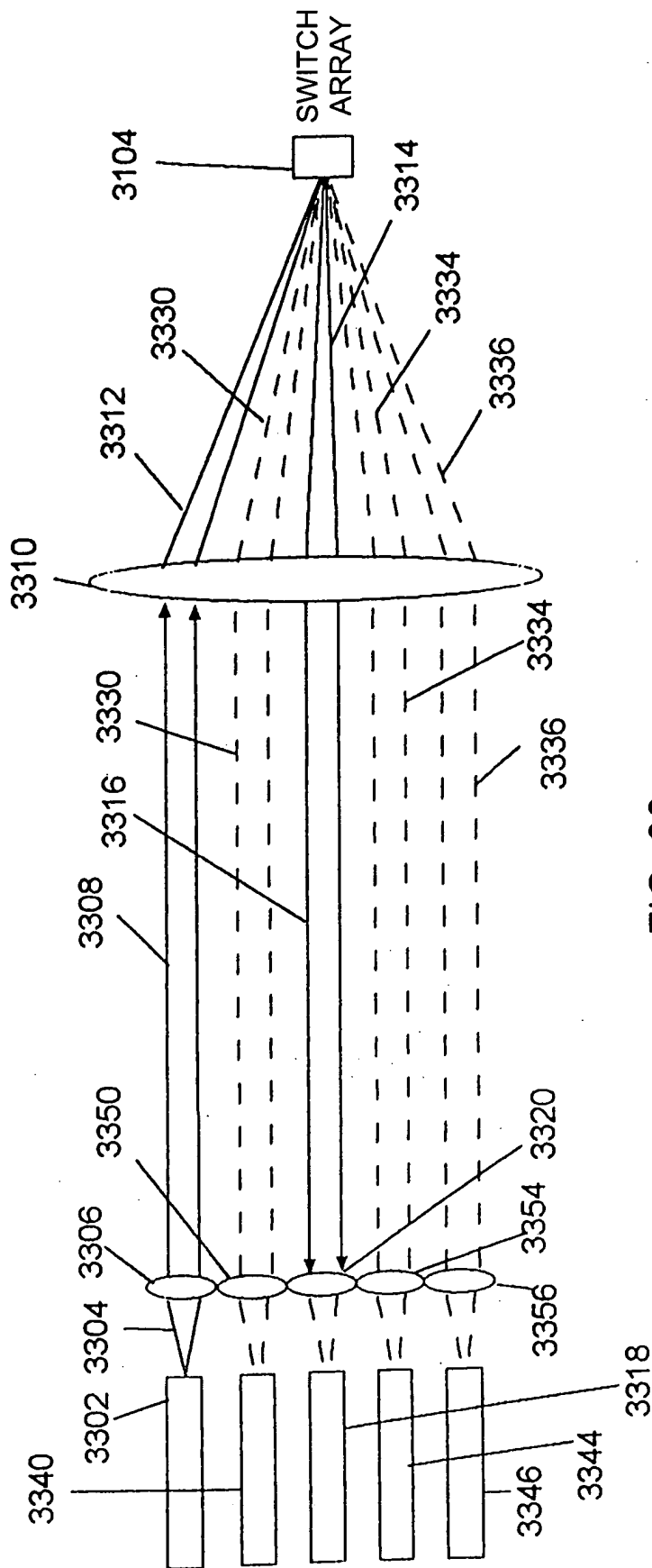


FIG. 33

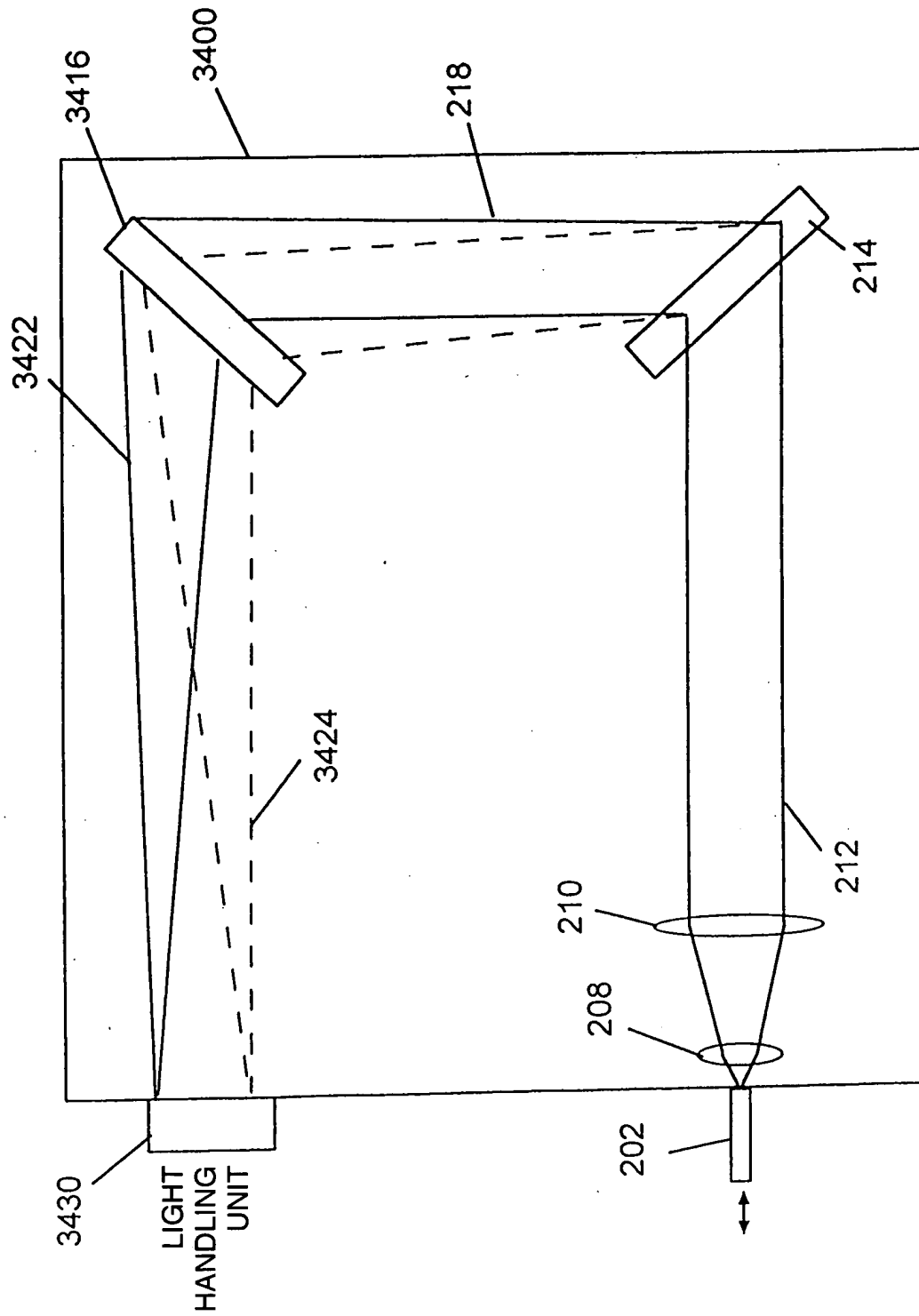
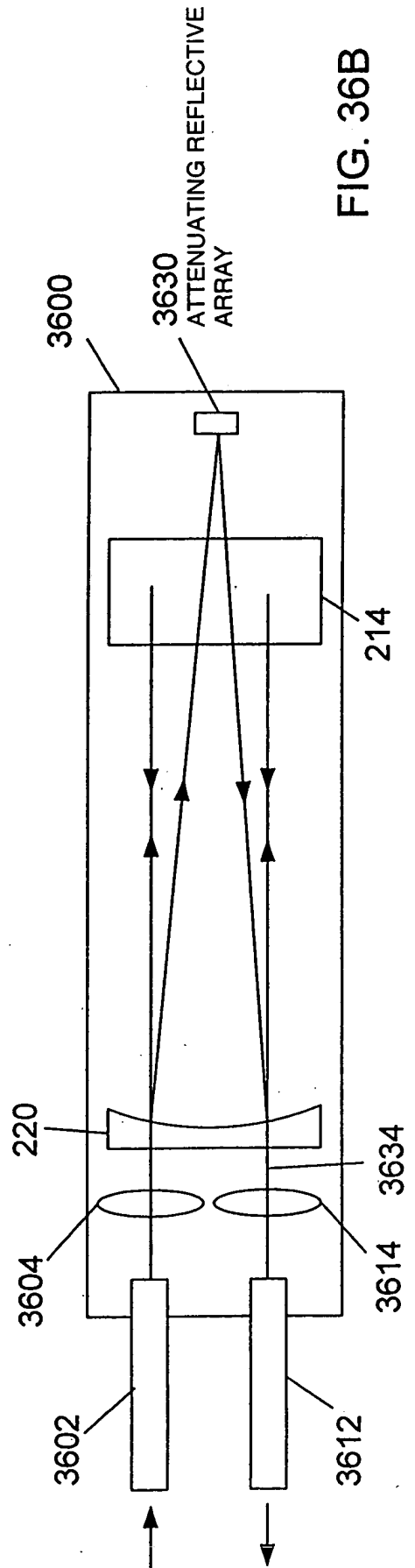
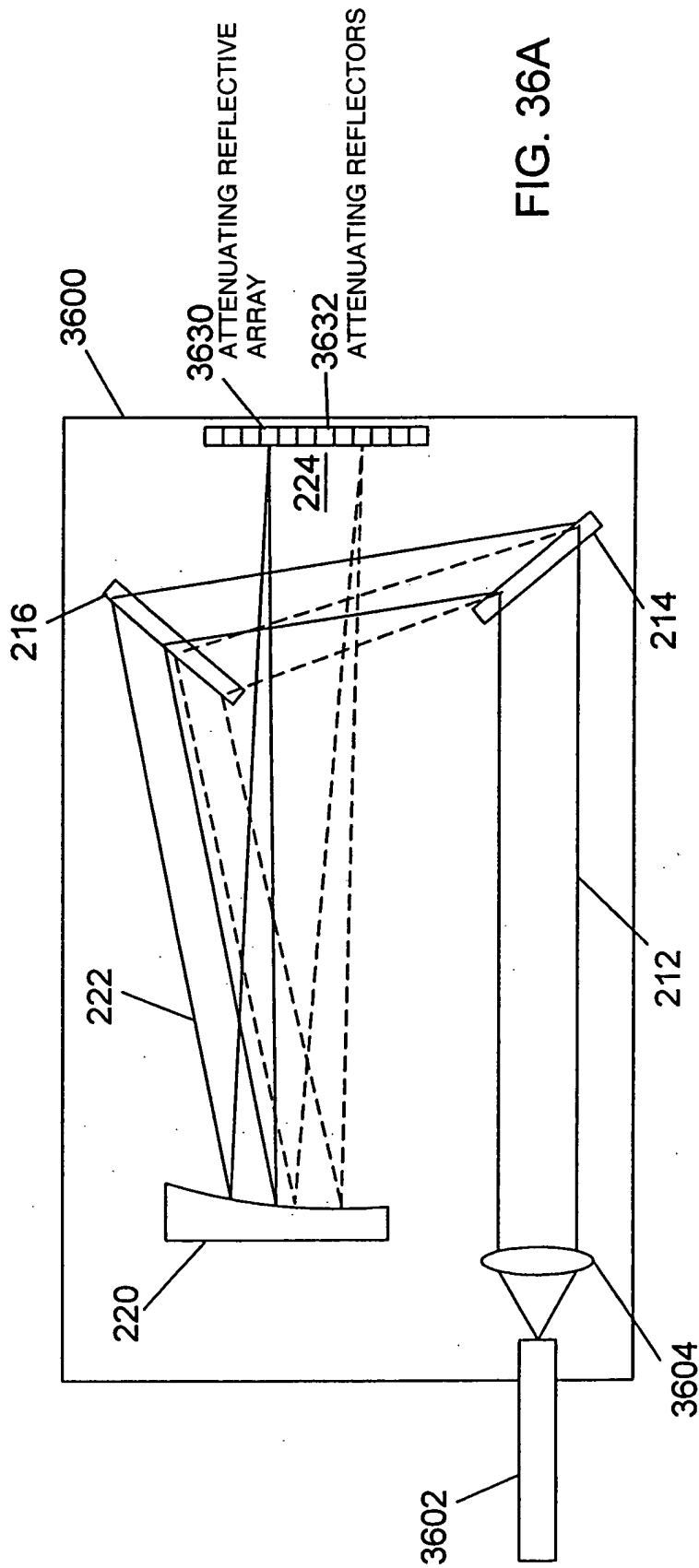
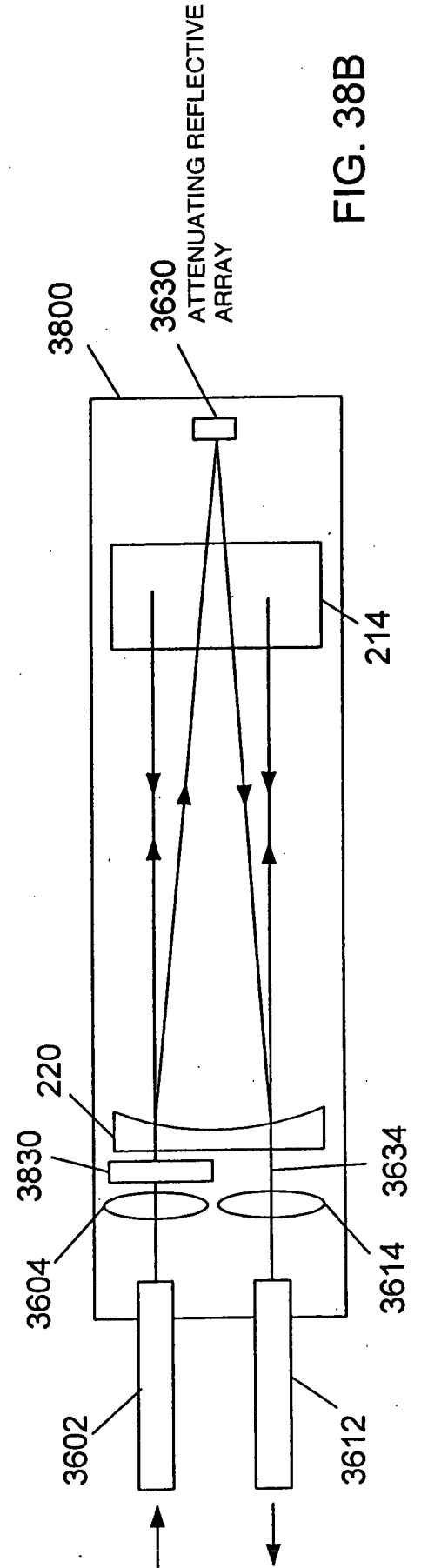
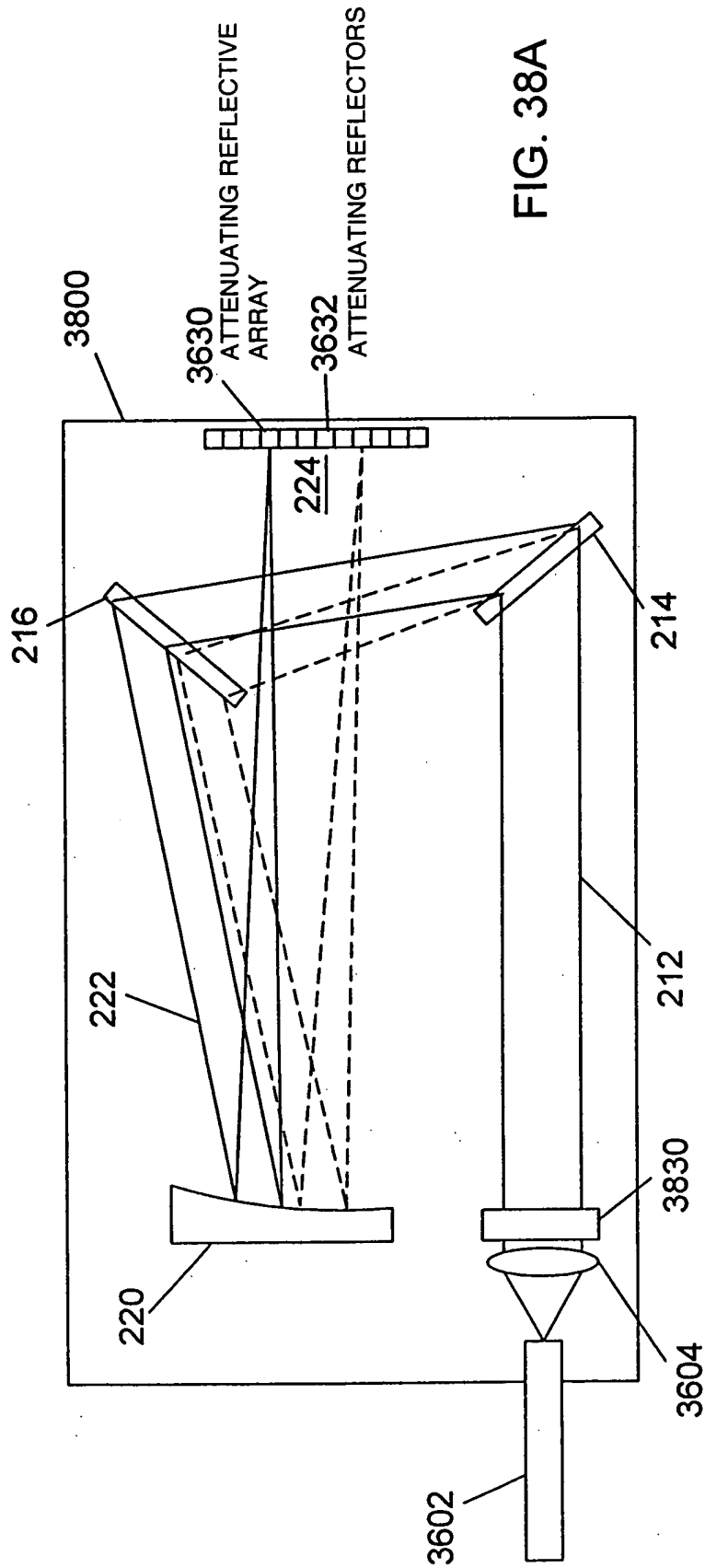


FIG. 34





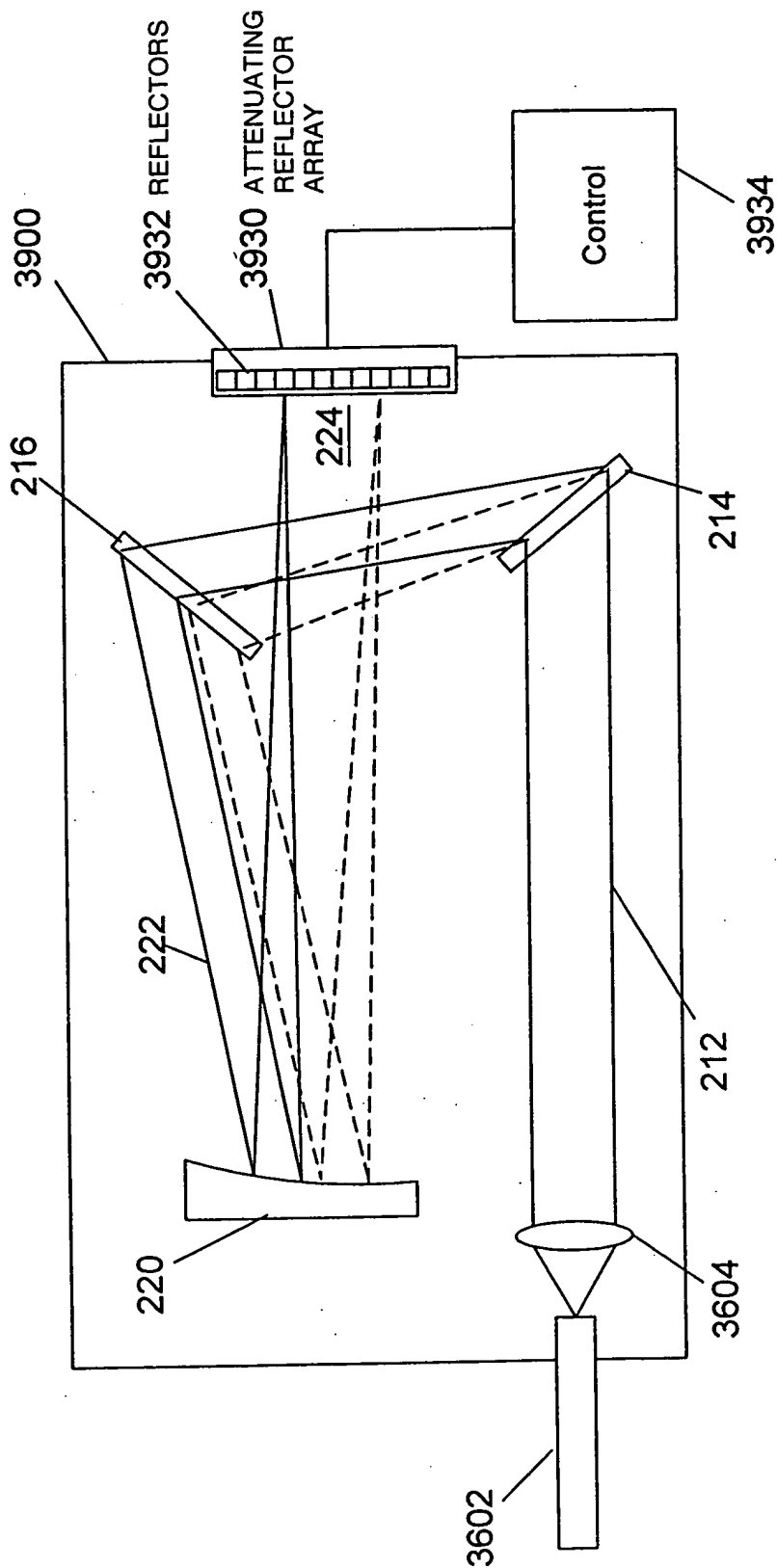


FIG. 39

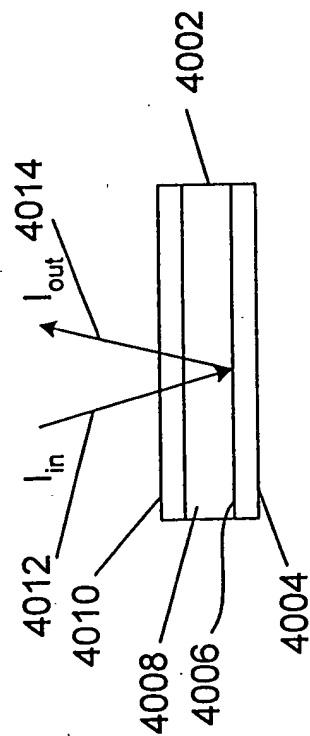


FIG. 40